## ACKNOWLEDGEMENT RECEIPT

Electronic Version 1.1 Stylesheet Version v1.1.1

Title of Invention

FFS ID:

METHOD OF FORMING A LOW VOLTAGE GATE OXIDE LAYER AND TUNNEL OXIDE LAYER IN AN EEPROM CELL

Submision Type: Information Disclosure

Statement

Application Number:

10/717149

Confirmation

88332

Server Response:



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Message

application, contact the Patent Electronic Business Center: Toll-Free Number:1(866) 217-9197 Website:

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First Named Applicant: Alan Renninger

Attorney Docket Number: ATM-262

Timestamp: 2005-07-18 12:07:55 EDT

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File Listing:

| Doc. Name    | File Name           | Size (Bytes)  |            |
|--------------|---------------------|---------------|------------|
|              |                     |               | Produced   |
|              |                     |               | (yyyymmdd) |
| us-ids       | Atm262-usidst.xml   |               | 2005-07-18 |
| us-ids       | us-ids.dtd          | 7763          | 2005-07-18 |
| us-ids       | us-ids.xsl          |               | 2005-07-18 |
| package-data | Atm262-pkda.xml     | 1759          | 2005-07-18 |
| package-data | package-data.dtd    | 27025         | 2005-07-18 |
| package-data | us-package-data.xsl | 19263         | 2005-07-18 |
|              | 60601               | $\overline{}$ |            |

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